

**PROCESS FOR CREATING AN ELECTRICALLY ISOLATED
ELECTRODE ON A SIDEWALL OF A CAVITY IN A BASE**

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Patent Application of
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for
PROCESS FOR CREATING AN ELECTRICALLY ISOLATED ELECTRODE ON A
SIDEWALL OF A CAVITY IN A BASE

10 CROSS-REFERENCE TO RELATED APPLICATION

This application is a divisional application serial no. _____, Agents Docket Number ONX-115A, entitled "MEMS Mirrors With Precision Clamping Mechanism," filed April 12, 2001, which is based on and claims priority from Provisional application 60/250,081 filed November 29, 2000.

15 FIELD OF THE INVENTION

This invention relates generally to microelectromechanical structures (MEMS). More particularly, it relates to a clamping mechanism for MEMS apparatus.

20 BACKGROUND OF THE INVENTION

MEMS free-space optical switches can be categorized into two major branches: the planar matrix (2-dimensional) approach, and the beam-steering (3-dimensional) approach. The 2D approach typically involves mirrors that move between on and off position. The angular accuracy at the on position is extremely critical as it affects the alignment of the mirror and optical loss of the switch.

30 Using <110> silicon with anisotropic etchants, one can form trenches with 90-degree sidewalls. If one bonds this wafer to another wafer that has free rotating mirrors, the sidewall can serve as a reference stopping plane to fix the up- mirrors in

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a vertical position. In addition, the sidewall may also serve as an electrode for electrostatically clamping the mirror in the vertical position.

5 One type of optical switch employs microelectromechanically-actuated mirrors. Fig. 1 depicts one type of MEMS actuated switch 100 that is made using 2 substrates. A top chip 101 containing a sidewall for receiving a movable mirror 111 is bonded to a bottom chip 102 containing a base 103. There are a few complications associated with the two-wafer approach. The attachment process requires a very high accuracy aligner-bonder. Moreover, the two-chip process places certain geometrical constraints that limit the minimum geometry of the trenches and mirrors. Furthermore, the complexity of the fabrication and alignment process can increase cost and reduce yield.

Therefore, there is a need in the art for a low-cost, high-yield scalable switch and a process of fabricating same.

SUMMARY OF THE INVENTION

The disadvantages associated with the prior art may be overcome by a process for creating an electrically isolated electrode on a sidewall of a cavity in a base. The process generally involves etching one or more trenches in a backside of a base, e.g., by anisotropic etch. The base may be a crystalline material, e.g., crystalline silicon having a <110> crystal orientation. The trench may be etched such that an orientation of the sidewall is defined by a crystal orientation of the base material. A layer of insulating material is formed on one or more sidewalls of one or more of the trenches. A conductive layer is formed on the layer of insulating material on one or more sidewalls of one or more of the trenches. Base material is removed from a portion of the

base bordered by the one or more trenches to form a cavity in the base. The trenches may be defined underneath a flap that overlies the base. The trench etch may stop on an etch-stop layer so that the cavity does not form all the way through the base. The conductive layer may completely fill up the trench between the insulating materials on the sidewalls to provide the isolated electrode. Conducting material may also be deposited on the backside of the base to provide electrical connections or electrodes on that side.

BRIEF DESCRIPTION OF THE FIGURES

Fig. 1 is a cross-sectional schematic diagram of a MEMS mirror apparatus according to the prior art.

Fig. 2A is an isometric schematic drawing of a MEMS apparatus according to a first embodiment of the present invention.

Fig. 2B is an isometric schematic drawing of a MEMS apparatus according to an alternative version of the first embodiment of the present invention.

Fig. 3A depicts a simplified cross-sectional schematic diagram of a MEMS apparatus according to another alternative version of the first embodiment of the present invention.

Fig. 3B depicts a simplified cross-sectional schematic diagram of a MEMS apparatus according to another alternative version of the first embodiment of the present invention.

Figs. 4A-4I depict a series of simplified cross-sectional diagrams illustrating the formation of a MEMS apparatus according to a second embodiment of the invention.

Fig. 5 is a simplified cross-sectional diagram depicting a portion of the apparatus of Fig. 4I.

Fig. 6 depicts an optical crossbar switch that uses an array of MEMS mirrors according to a third embodiment of the present invention.

DETAILED DESCRIPTION

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The disadvantages associated with the prior art may be overcome by a microelectromechanical (MEMS) apparatus having a base and a flap with a bottom portion coupled to the base so that the flap may move out of the plane of the base between first and second angular orientations. An array of one or more of such structures may be used to form an optical switch. The base may have an opening with largely vertical sidewalls containing one or more electrodes. The sidewalls contact a portion of the flap when the flap is in the second angular orientation. The electrodes may be electrically isolated from the base. The flap may include a magnetic material so that the flap moves in response to an external magnetic field. A voltage source may be coupled between the flap and the sidewall electrode to apply an electrostatic force between the sidewall electrode and the flap such that the flap assumes the angular orientation of the sidewall. The electrostatic force may be sufficient to prevent the flap from changing position in the presence of an applied magnetic field. The apparatus may further include an electrode on the base and a voltage source coupled between the electrode in the base and the flap to apply an electrostatic force between the electrode in the base and the flap. The base may be made from a substrate portion of a silicon-on-insulator (SOI) wafer and the flap defined from a device layer of the SOI wafer. The flap may be connected to the base by one or more flexures such as torsional beams.

A MEMS apparatus of the type described above may be provided with one or more conductive landing pads on the underside of the flap that are electrically isolated from the flap. The landing pads may be electrically coupled to either the sidewall electrode or the base to reduce stiction and arcing. Alternatively, conductive landing pads may be provided on the sidewall or base that are equipotential with the flap.

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A first embodiment of the invention is shown in Fig. 2A, which shows an apparatus 200 having a movable flap that can be precisely clamped by electrodes on either 0 or 90 degrees surfaces. Such a structure allows the flap to be clamped, for example, in either a vertical or horizontal position. Such a flap may be used as part of an array of several MEMS mirrors in a planar matrix switch.

The apparatus 200 generally comprises a base 206 and a flap 211 coupled to the base 206, e.g. by one or more flexures 214, so that the flap 211 is movable out of the plane of the base 206 from a first angular orientation to a second angular orientation. By way of example, the first position may be substantially horizontal, i.e., substantially parallel to a plane of the base, and the second position may be substantially vertical, i.e., substantially perpendicular to the plane of the base. The flap 211 may include a light-deflecting element 213 so that the apparatus 200 may operate as a MEMS optical switch. By way of example, the light-deflecting element 213 may be a simple plane reflecting (or partially reflecting) surface, curved reflecting (or partially reflecting) surface, prismatic reflector, refractive element, prism, lens, diffractive element, e.g. fresnel lens, a dichroic coated surface for wavelength specific and bandpass selectivity, or some combination of these. The flap 211 and the base 206 may be formed from a portion of a starting material 201 in order to avoid alignment problems associated with post-process bonding associated with a two wafer approach. For example, the starting material 201 may be formed from a silicon-on-insulator (SOI) wafer having a device layer 202, an insulator layer 204 and a substrate layer as the base 206. The starting material 201 may include an opening or cavity 215 having sidewalls 217 that are vertical, i.e., substantially perpendicular to a plane of the base 206. One

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or more of the sidewalls 217 may contain an electrode 216 that may be electrically isolated from the base 206. The flap 211, flexures 214, and sidewalls 217 may be positioned so that a bottom portion of the flap 211 contacts one of the sidewalls 217 when the flap 211 is in the second angular orientation such that the flap 211 may assume an orientation substantially parallel to that of the sidewall 217. A voltage applied between the electrode and the flap may attract the flap to the sidewall to secure the flap in place. Preferably, the flap 211 is attracted to the electrode 216 such that the flap 211 may assume the angular orientation of the sidewall 217.

Any conventional means may be used to provide an actuating force to move the flap 211. For example, the flap 211 may contain a magnetically active element 240 to facilitate movement of the flap by interaction with an externally applied magnetic field. The magnetically active element 240 may be a magnetically active material having, e.g. a fixed magnetic moment, i.e., it may be a permanent magnet. Magnetically active materials may include Nickel, Nickel-Iron, Iron-Cobalt, Aluminum-Nickel-Cobalt, Neodymium-Iron-Boron, etc., and, may be deposited in a uniform or stepped pattern.

The inventors have discovered that a stepped pattern of magnetically active material may be deposited to a movable flap such as the flap 211. It must be stated that a stepped magnetic material may be used with any moveable flap. The stepped pattern may increase the amount of torque applied to the flap when exposed to a magnetic field. For example, the thickness and/or profile of the magnetic material may be varied by sequentially depositing slabs of material of comparable thickness to produce a series of steps. The height of the steps may vary along a direction perpendicular to an

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axis of rotation of the flap. The stepped magnetic material may act as a guide for the magnetic field and thereby enhance the torque or force exerted on the flap by the field. The configuration of the steps may depend on the relative orientation of the magnetic field with respect to the flap and the rotation axis of the flap. By way of example, where the rotation axis is disposed along an edge of the flap and the magnetic field is perpendicular to both the rotation axis and a plane of the flap, e.g. the horizontal plane, to enhance the torque to rotate the flap upwards out of the horizontal plane the steps may rise away from the axis. In other words, portions of the magnetic material that are close to the axis are lower than portions that are further away. To enhance the torque for a downward rotation the steps may rise toward the rotation axis, i.e., portions of the magnetic material that are closer to the axis may be lower than portions that are further away.

The flexures 214 may apply a torsional, or restoring force that returns the flap 211 to the first position when the actuating force is removed. However, other restoring forces may be applied to flap 211 to return the flap to the first position. Such torque may be exerted on flap 211 by biasing mechanisms that operate via pneumatic, thermal, or magnetic principals, including coils that interact with an external magnetic field, electrostatic elements, such as gap closing electrodes, piezoelectric actuators and thermal actuators. Multiple restoring forces may also used together, and the forces may operate along the same or opposing directions.

In one configuration, shown in Fig 2B an apparatus 200B may include a flap 211' having magnetically active element 240' that includes one or more coils 220 instead of, or in addition to, a magnetic material. The coils 220 may interact with an

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externally applied magnetic field **H**. The magnetic field **H** may be applied by a magnetic field source **225**, which may be any suitable source of magnetic field, e.g. an external coil or permanent magnet. By way of example, Fig. **2B** depicts a magnetic field that is substantially horizontal. Alternatively, the magnetic field **H** may have any orientation. Electric current applied through the coil **220** interacts with the external magnetic field **H** in a way that causes a flap **211'** to move from one angular position to another with respect to a base **206'**. In a particular configuration, the coil **220** may interact with a magnetic material deposited in close proximity to the flap **211'**. The magnetic material may be applied to a sidewall **217'** of the base **206'**. The magnetic material may be applied through suitable techniques such as sputtering or electroplating. In configurations where magnetically active element **240'** includes a coil **220**, the polarity of current that runs through the coil **220** may be reversed to apply an opposite force to the flap **211**.

By way of example, the one or more coils **220** may be fashioned by forming an insulating layer on the flap **211**, etching one or more trenches in the insulating layer, e.g. in a spiral shape, and filling the one or more trenches with electrically conductive material such as aluminum or copper.

In an alternative embodiment, stiction, e.g., between the flap **211** and the base **206**, may be reduced by applying a pre-bias force to the flap to move the flap at least partially out of contact with an underlying base. By way of example, the magnetically active element **240** may interact with a fixed pre-bias magnetic field. The pre-bias magnetic field may exert a force on the magnetically active material **240** that produces a biasing torque on the flap **211**. The biasing torque may partially counteract a mechanical or other torque exerted on

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the flap 211. As a result, when the flap 211 is in the first position, it is moved slightly out of a position parallel with the plane of the base 206. Consequently, the flap 211 does not touch an underlying portion of the base 206. Thus, the effects of stiction and squeeze-film damping may be reduced.

It must be stated that a pre-bias force may be applied to a variety of movable MEMS devices, including prior-art MEMS mirrors and flaps, to move the device at least partially out of contact with an underlying base to reduce effects of stiction. Furthermore, it must be stated that the pre-bias force may be generated by several biasing elements, including but not limited to flap torsion springs, current carrying coil, gap-closing electrodes, spring loaded element, stress bearing material, piezoelectric element and thermal bimorph actuator.

The flap 211 may include a light-deflecting portion 213 so that the apparatus may be used in a planar matrix switch. Where such an apparatus is used in a planar matrix switch, it is desirable to be able to clamp the flap 211 at 2 different positions. Between these two positions, the accuracy of an ON position, e.g. where the flap 211 is vertical, is of particular importance. In a particular embodiment of the present invention, the flap 211 moves out-of-plane by magnetic actuation and is clamped in place by electrostatic attraction to an electrode. In a similar fashion, the mirror may be held in-plane by another set of electrodes or by a voltage difference between the flap 211 and the base 206.

The state or position of a flap such as the flap 211 may be sensed by one or more sensors including gap closing electrodes, capacitive, inductive, or piezoresistive elements, strain gauges, coils, magnets, optical sensors, and the like.

The invention is not limited to flaps that move upwards into an "on" position. For example, an alternative embodiment of a MEMS apparatus 300 is depicted in Fig. 3A. The apparatus 300 may incorporate all the main features of the apparatus of Fig. 2. The apparatus generally includes a substrate 301 having a device layer 302, insulator layer 304 and base 306. A cavity 315 formed through the substrate 301 includes a sidewall electrode 316, and a flap 311 movably connected to the base 306 by a flexure 314. The flap 311 moves, i.e. translates and moves, downwards into the through-wafer cavity 315, e.g. under the influence of a magnetic field H.

The flap may include a reflecting or other optical element 313. The apparatus 300 may act as a mirror. The flap 311 may further include one or more electrically conductive landing pads 322 that are electrically connected to the sidewall electrode 316. The landing pads 322 may be electrically isolated from the flap 311 by an insulating material 323. By maintaining the landing pads 322 substantially equipotential to the sidewall electrode 316, stiction and arcing between the landing pads and sidewall electrode may be reduced. Alternatively, as shown in Fig. 3B an apparatus 300' of the type described above with respect to Fig. 3A may include one or more conductive sidewall landing pads 324 that are electrically isolated from a sidewall 317' and electrically coupled to a flap 311'.

Figs. 4A-4H depict an example of a process for fabricating a structure of the type shown in Fig. 2A, Fig. 2B, Fig. 3A or Fig. 3B. The structure is formed from a starting material wafer 400 having a device layer 403, a substrate layer 401 and an insulator layer 402 disposed between the device and substrate layers as shown in Fig. 4A. The base and/or device

layer may be made of a crystalline material. For example, the starting material is typically a silicon-on-insulator (SOI) wafer with a silicon <110> handle substrate as the substrate layer 401; the device layer can be standard silicon of <100> orientation; and the insulating layer can be an oxide formed, e.g., by oxidation of a surface of the substrate layer 401. Alternatively, the starting material may be a silicon-on-nitride wafer or any other suitable type of wafer material known to the art. The device layer 403 may be used, for example to form a mirror plate for a MEMS optical switch, or an array of such switches, such as that shown in Fig. 6. In the first step Fig. 4B, two parallel deep trenches 404 that reach the buried oxide 402 may be formed in the substrate layer 401 from the backside of the wafer 400. The widths of the trenches 404 may be sufficiently narrow that they can be completely filled-in during a later step. The trenches 404 may define the periphery of a cavity 415 that may be formed to accommodate a flap similar to those described above with respect to Figs 2-3B. It is also possible to form a single continuous trench that defines the periphery of the cavity. The trench or trenches 405 may be etched by an anisotropic etchant like KOH with silicon nitride or silicon oxide mask (not shown for simplicity). Alternatively, the trenches 405 may be formed in the substrate layer 401 prior to bonding the device layer 403 to the substrate layer 401.

In the next step, as depicted in Fig. 4C, an insulator layer 405 may be deposited or grown that covers the interior surface of the trenches and the wafer backside. The insulator layer 405 may preferably be a conformally deposited layer (e.g. TEOS oxide, thermal oxide or silicon nitride). Next, as shown in Fig. 4D, a conformal conductive layer 406 may be deposited over the insulator layer 405 on the interior surface of the trenches 404. In a preferred implementation, the conductive

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layer 406 completely fills the trenches 404. The conductive layer 406 may be made of any conductive material, including, but not limited to a layer of metal or doped semiconductor material. By way of example, the conductive layer 406 may be polycrystalline silicon (polysilicon), which is a good candidate material for filling the trenches 404. Alternately, tungsten, titanium nitride, or silicon carbide may also be used. After this step, the starting material 400 is substantially free of any large or deep holes and trenches and may be compatible with additional standard wafer fabrication processes.

Next, as shown in Fig. 4E, the conductive layer 406 may be patterned and etched to expose the insulator 405 directly under the to-be-formed cavity. Then a protective material 407 may be deposited on both a front side (i.e. the exposed surface of the device layer 403) and a backside of the starting material 400. By way of example, the protective material 407 may be a layer of silicon nitride. A layer of silicon nitride can serve as an etching mask on the backside 409 of the starting material 400 (i.e., the exposed surface of the substrate layer 401) in a later step. Silicon nitride can also be used as an insulator for the front side of MEMS structures. Alternately, silicon dioxide or a polymer may also be used as the protective material 407.

Next, a hinged flap 410 may be formed from the top device layer 403 as shown in Fig. 4F. The flap may optionally contain landing pads 408 on its bottom surface to minimize contact area with the base or sidewall and to provide electrical isolation as described above. The flap 410 may be mechanically connected to the base through a flexure or torsional beam similar to those depicted and described with respect to Fig. 2A, allowing the flap 410 to move out of the

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plane of the base. A reflecting surface 410 may be formed on the surface of the flap 410 so that the resulting device may function as a mirror.

5 Electrically isolated conductive landing pads 422 may be formed on an underside of the flap 410. For example vias may be etched through the flap into the oxide 402. The vias may then be partially or completely filled with insulating material. The vias may then be filled with conductive material.

Up to this point the starting material 400 is free of deep holes. As a result, the starting material 400 is compatible with standard wafer fabrication processes and is robust and less likely to be damaged during handling and processing.

At this point a cavity containing a clamping electrode and mechanical stop has been defined. In the next step, as shown in Fig. 4G, the backside masking layer (protective layer 407) may be patterned and an opening 411 may be etched through the backside insulator 405 to expose the surface of the base 401. In the following step, as shown in Fig. 4H, a selective etch process that does not attack the insulator materials (405 and 407) may be used remove the bulk silicon and forms a cavity 25 415 having sidewalls 416. During this step, the conductive material 406 can be well protected by the insulator materials 405 and 407 and stays intact. The conductive material 406 that filled the trenches 404 forms sidewall electrodes 417 that are electrically insulated by the material of layer 407.

30 The bulk material from the base 401 may be removed by any suitable etch process. For example an anisotropic etch, using an etchant such as KOH or other anisotropic etchant (e.g., EDP, etc) may be used. Alternatively, an isotropic silicon

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etch (e.g., a mixture of Nitric acid, Hydrofluoric acid, Acetic acid, and water) may be used. Furthermore, a vapor etch, e.g. using XeF_2 or SF_6 vapor may be used to etch out the bulk silicon material to form the cavity 415. Finally, a sacrificial etch may remove selected portions of insulator layers 405 and 402 to release the flap 410, and form a completed MEMS device 490 as shown in Fig. 4I. The flap may still be attached to the rest of the device layer 403 and to the base 401 by one or more flexures similar to those shown and described above with respect to Fig. 2A. Preferably, the flexures are sufficiently flexible to permit the mirror plate to move out of the plane of the base.

Fig. 4I shows that the sacrificial layer 402 is completely removed from beneath the flap 410. However, in an isotropic etch process, the sacrificial layer 402 may be only partially removed from beneath the other portions of the device layer 403 leaving them undercut. The amount of undercut may be controlled, e.g. by the timing of the etch process. Alternatively, an etch stop material may be used to limit the undercut.

Fig. 5A depicts a cross-sectional schematic diagram of the device 500 that has been fabricated according to a process of the type depicted in Figs. 4A-4I. Fig. 5A shows the advantages of this process. The device 500 has a base 501 with a cavity 515. The device 500 includes a flap 510 that is free to move with respect to a plane of the base 501. The flap 510 may include a reflecting element 520. The device 500 includes sidewall electrodes 517A, 517B that may be electrically isolated from the base 501. Voltage sources V_1 , V_2 , V_3 , can apply independent potentials to the base 501 and electrodes 516A, 516B respectively. The base 501 may also be regarded as an electrode if it is electrically conducting. Alternatively,

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an electrically isolated electrode (not shown) may be used to clamp the flap 510 to the base when it is in a substantially horizontal position. The voltage sources V_1 , V_2 , V_3 produce electric fields between the flap 510 and the base 501 or sidewall electrode 516A that can clamp or release the flap 510 independently. This may be an important feature for MEMS mirror actuation. Moreover, the sidewall electrodes 516A, 516B on opposite sidewalls 517A, 517B of the cavity 515 can also be electrically isolated from each other by patterning the backside conductor 506 to define separate leads to each electrode 516A, 516B.

The device 500 may include one or more conductive landing pads 522 that may be used to reduce stiction between the flap 510 and the sidewall 517A or the base 501. Such landing pads 522 may be electrically isolated from the flap 510, e.g., by an insulating material, and electrically coupled to a landing surface, e.g., either the base 501 or the sidewall 517A. The electrical connection may be maintained in various configurations, including those where the conductive landing pads 522 are substantially the same electric potential as the landing surface. This reduces the risk of arcing that can damage the landing surface or microweld the flap to the landing surface. The base 501 may therefore serve as an independent electrode for clamping the flap 510 in a position parallel to the plane of the base 501.

Alternatively, as shown in Fig. 5B, a device 500', of the type described with respect to Fig. 5A, may achieve the same result by including one or more landing pads 524 on a base 501' and/or sidewall 517A' that are electrically isolated from the base 501' and a sidewall electrode 516A' yet may be electrically connected to flap 510'. The base 501' may therefore serve as an independent electrode for clamping the

flap 510' in a position parallel to the plane of the base 501'.

The present invention includes systems that incorporate two or more MEMS apparatus, e.g. arranged in an array. Such an array is depicted in the crossbar switch 600 of Fig. 6. The switch 600 generally comprises an array of MEMS mirrors 602 of having features in common with MEMS devices of one or more of the types depicted in Figs. 2A-2B, Fig. 3 or Fig 5. Specifically, each mirror 602 includes a flap having a portion coupled to a base 601 so that the flap is movable out of the plane of the base 601 from a first angular orientation to a second angular orientation. Each flap may contain a light deflective element. The base 601 has one or more openings to receive one or more flaps. Each opening has largely vertical sidewalls. The sidewalls contact a portion of the flap such that the flap assumes an orientation substantially parallel to that of the sidewall when the flap is in the second angular orientation. The sidewalls may contain one or more electrodes for clamping the mirrors 602 against the sidewalls. The mirrors 602 couple optical signals 604 between one or more input fibers 606 and one or more output fibers 608. Although the mirrors 602 depicted in the apparatus of Fig. 6 tilt up to deflect the light, those skilled the art will recognize that the mirrors may alternatively tilt downwards in a manner similar to that depicted in Fig. 3.

In accordance with the foregoing, low-cost, high yield scalable switches may be provided without the disadvantages attendant to a two-chip design. It will be clear to one skilled in the art that the above embodiment may be altered in many ways without departing from the scope of the invention.